

Karamadai, Coimbatore - 641104

PERMISSION LETTER

Date:09.02.2023

From

Mrs. V. Nithya

Assistant Professor / Course Coordinator

Department of Electronics and Communication Engineering

Christ the King Engineering College

Coimbatore

To

The Principal

Christ the King Engineering College

Coimbatore

Through: Head of The Department / Electronics and Communication Engineering

Respected Sir,

Sub: Requesting permission to organize a Value-Added Course on "MSP430" - Reg.

Department of Electronics and Communication Engineering is planning to organize a Value-Added Course on "MSP430" from 13.02.2023 to 04.03.2023 for Third and Final year ECE students. Ms.L.Pavithra, Assistant Professor, Department of Electronics and Communication Engineering as a Resource Person. In this regard, we seek your permission to conduct the same in the Seminar Hall from 13.02.2023 to 04.03.2023 from 4 pm to 7 pm. Kindly consider our request and grant the same.

Thanking You,

Approved

Dr.M.JEYAKUMAR, M.E.Ph.D.
PRINCIPAL

CHRIST THE KING ENGINEERING COLLEGE, Chikkarampalayam Viliage, Karamadai, Mettupalayam Taluk, Coimbatore - 641 104.



Karamadai, Coimbatore - 641104

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

RefNo: CKEC/ECE/VAC/2022-23/07

DATE:10.02.2023

CIRCULAR

This is to inform that the department of Electronics and Communication Engineering is planning to conduct a value-added course on "MSP430" for all third and final year students on 13.02.2023 to 04.03.2023 Students are asked to register you and utilize this opportunity to improve their skills.

S. No	Course Code	Name of the Course	Department	No of Hours	Course Coordinator
1	VACEC2307	MSP430	ECE	32	Ms.L.Pavithra

Note: The circular Display in the department Notice board for student's attention.

Copy to:
Principal
Notice board

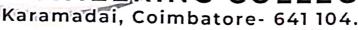
Vn⁄Q H₀D/ECE

KUMAR, M.E..Ph.D.



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DEPARTMENT OF ECE

Organizes

Value Added Course on MSP430

13.02.2023 to 04-03-2023 (4 to 7pm)

Resource Person

Ms. L. Pavithra Assistant Professor Department of ECE Engineering

Chief Patron

Dr. M. Jeyakumar

Principal

Patron

Dr. V. Muralidharan
HOD / ECE
Coordinator
Mrs. V. Nithya
AP / ECE



VENUE: SEMINAR HALL

Dr.M.JEYAKUMAR, M.E.Ph.D.
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Course Code	VACEC2307
Name Of the Course	MSP430
Duration Of the Course	32 Hours
Academic Year	2022-2023
Period Of the Course	16 Days

COURSE OBJECTIVES:

- Understanding the architecture and instruction set of the MSP430.
- Developing skills in debugging and troubleshooting MSP430-based systems
- Gaining knowledge of advanced features and applications of the MSP430, such as analog-todigital conversion and communication protocols.
- Understanding how to interface external devices and sensors with the MSP430

Syllabus

UNIT I- MSP430 Architecture

(6)

Introduction – Where does the MSP430 fit, The outside view, The inside view-Functional block diagram. Memory, Central Processing Unit, Memory Mapped Input and Output, Clock Generator, Exceptions: Interrupts and Resets, MSP430 family.

UNIT II -- Addressing Modes & Instruction Set

(6)

Addressing Modes, Instruction set, Constant Generator and Emulated Instructions, Program Examples

UNIT III - Clock System, Interrupts and Operating Modes

(6)

Clock System, Interrupts, what happens when an interrupted is requested, Interrupt Service Routines, Low Power Modes of Operation, Watchdog Timer, Basic Timer1, Real Time Clock, Timer-A: Timer Block, Capture/Compare Channels, Interrupts from Timer-A

UNIT IV - Analog Input-Output and PWM

(6)

Comparator-A, ADC10, ADC12, Sigma-Delta ADC, Internal Operational Amplifiers, DAC, Edge Aligned PWM, Simple PWM, Design of PWM.LCD interfacing

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COMPATORE SOLUTION

No



Karamadai, Coimbatore - 641104

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UNIT V - Digital Input-Output and Serial Communication >

Peripherals in MSP430, Serial Peripheral Interface

(7)

Parallel Ports, Lighting LEDs, Flashing LEDs, Read Input from a Switch, Toggle the LED state by pressing the push button, LCD interfacing.

Asynchronous Serial Communication, Asynchronous Communication with USCI_A, Communications,

TOTAL HOURS:31

COURSE OUTCOME:

- Understand the MSP430 architecture, including its CPU, memory, and peripheral modules.
- acquire skills in debugging and troubleshooting MSP430-based systems, including identifying and resolving common hardware and software issues. Gaining knowledge of advanced features and applications of the MSP430, such as analog-to-digital conversion and communication protocols.
- Understand interfacing external devices and sensors with the MSP430 microcontroller, utilizing various communication protocols such as UART, SPI, and I2C.
- Students should demonstrate the ability to work effectively in teams, collaborating with peers on group projects to achieve common objectives related to MSP430-based systems

TEXT BOOK & REFERENCES:

- 1. John H. Davies., "MSP430 Microcontroller Basics", Newnes, 2008
- 2. "MSP430 Family Guide", Texas Instruments, 2018

SAG ENGINEERING COMORDOSE STORES

Dr.M.JEYATUMAR, M.E.Ph.D PRINCIPAL CHRIST THE KING ENGINEERING COLLEGE, Chikkurampalayam Vinoye, Karamadal, Mempalayam Taluk, Combatare - 641-104.



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING VAEC2307 - MSP430 SCHEDULE

S. No	DAY	TIME/SESSION	CONTENT	RESOURCE PERSON
	13.02.2023	4.00-6.00pm	MSP430 Architecture	
			Introduction – Where does the MSP430 fit, The	
	14.02.2023	4.00-6.00pm	outside view, The inside view-Functional block	
1			diagram, Memory, Central Processing Unit,	
	15.00.000	4.00 4.00	Memory Mapped Input and Output, Clock	
	15.02.2023	4.00-6.00pm	Generator, Exceptions: Interrupts and Resets,	
			MSP430 family	
	16.02.2023	4.00-6.00pm	Addressing Modes & Instruction Set	Ms.Pavithra.L
	17.02.2023	•	Addressing Modes, Instruction set, Constant	Assistant
2	17.02.2023	4.00-6.00pm	Generator and Emulated Instructions, Program	Professor
	20.02.2023	4.00-6.00pm	Examples	Department of
	21.02.2023	4.00-6.00pm	Clock System, Interrupts and Operating	Electronics and
		•	Modes	Communication
	22.02.2023	4.00-6.00pm	Clock System, Interrupts, what happens when	Engineering
			an interrupted is requested, Interrupt Service	Christ the King
3			Routines, Low Power Modes of Operation,	Engineering
	22 02 2022	4.00.6.00	Watchdog Timer, Basic Timerl, Real Time	College
	23.02.2023	4.00-6.00pm	Clock, Timer-A: Timer Block,	
			Capture/Compare Channels, Interrupts from	
			Timer-A.	
	24.02.2023	4.00-6.00pm	Analog Input-Output and PWM	
	27.02.2022	4.00.6.00===	Comparator-A, ADC10, ADC12, Sigma-Delta	
4	27.02.2023	7.02.2023 4.00-6.00pm ADC, Internal Operational Amplifiers, DAC,		
	28.02.2023	4.00-6.00pm	Edge Aligned PWM, Simple PWM, Design of	
	20.02.2025		PWM.LCD interfacing.	



Dr.M. JESTAN ME.Ph.D

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			Digital Input-Output and Serial
			Communication
	01.03.2023	4.00-6.00pm	Parallel Ports, Lighting LEDs, Flashing LEDs,
			Read Input from a Switch, Toggle the LED
			state by pressing the push button, LCD
	02.03.2023	4.00-6.00pm	interfacing. Asynchronous Serial
5	02.00.2020	25 Hot Groopin	Communication, Asynchronous
			Communication with USCI_A,
	03.03.2023	4.00-7.00pm	Communications, Peripherals in MSP430,
			Serial Peripheral Interface
	04.03.2023	4.00-5.00pm	Test

V∩Q H₀D/ECE



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING VAEC2307 - MSP430 STUDENT REGISTRATION

S.No	REGISTER NUMBER	NAME OF THE STUDENT	SIGNATURE
1	710420106001	BABU A	Pas
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3	710420106004	DHILEEPKUMAR S	Park
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING VAEC2307 - MSP430

STUDENTS ATTENDANCE LIST

CN	REGISTER	NAME OF THE	13.02.2023	14.02.2023	15.02.2023	16.02.2023	17.02.2023
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Dr.M.JEYARUMAR, M.E.Ph.D.
PRINCIPAL

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S.No	REGISTER NUMBER	NAME OF THE STUDENT	27.02.2023 4-6.00pm	28.02.2023 4-6.00pm	01.03.2023 4-6.00pm	02.03.2023 4-6.00pm	03.03.2023 4-7.00pm
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1	710420106001	BABU A	Tool hard	5005 h	S. Dave for	8.Ban 1-	5.3m =
2	710420106002	BABY INDIRA S	B. Bay-In	2000	13/11	PM. SI	TYPE
3	710420106004	DHILEEPKUMAR S	Hille	1	Kathy	15 PM	1111
4	710420106005	DIVYA S FIYARLIN SHARONE J		Col			
5	710420106006	HARRIS RAJA X		Fund	1200	12.10	Med &
7	710420106007	JAYA SRUTHI K	The state of the s	Taylor	They	Taus	TOURD.
8	710420106008	JEEVITHA P	Jays	Jaya	Jayo	Tooligh	Joseph
9	710420106009	JENIFA D	Joensto	Jerith	Jecutho	Leniter	Torolla
10	710420106010	JOSHUA THOMAS	zenite	geniter	A	Marie	terull
11	710420106011	KARTHIKA P <	To.	V	100	(2)	12)-
12	710420106012	KAVIMANI G	11.0	Main.	Josh .	Kain.	Karti
13	710420106013	MALATHI M	Many	Minute	May the	Dents	KUWH
14	710420106014	METREESHIYA S	State 10	Asia Or	The L	Madx	Miss
15	710420106013	MYTHILI A	AD .	20	The same	AD -	10 Danie
16	710420106017	NATARAJAN A	(10mls	(1) envil	(A) and	Munda	(sembl
17	710420106018	NIVETHA S	Winoth	Grante	(1) inth	Diver	Wineth
18	710420106019	PAULABISHEK D	Par :	Day	Limit	- Carlon	Dur
19	710420106020	PRADEEP S	5.00	SPEA	0.500	9.200	2.00
20	710420106021	PRAVEENKUMAR N	Praver	Proves	waver	fram	Pravi
21	710420106022	SANGAMITHRA T	0	B	A W	3	2
22	710420106028	SERMASAMY J	(Dans	6	(2)	(2)	12 00
23	710420106030	SHYAM PRAKASH S	2000	-CDD.	2012	000	Ym 1
24	710420106031	SWETHA S	lenetha	7 4	8	200	Date
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25	710420106032	, A 36 EN ACC 101	and the	- Marie	Thomas	mary	Lagua.
26	710420106033	VIGNESHWARAN A		VY	VY		VEY
27	710420106301	NAVEEN RAJ.P	X A T		Non	Xgw	TXONT
28	710420106302	SANTHOSH.S	5	× 1	Softer	130	7
29	710419106002	ARAVIND S E	And	A CONTRACTOR OF THE PARTY OF TH	(Destan	Acoust.	<u>Ontare</u>
30	710419106006	DHIVAKARAN M	Debl	ALC N	alla	Drox	D 108
31	710419106007	DINESH KUMAR S	Die:	Du	7724	2.	724
32	710419106008	GAYATHIRI	Sino	- Bul-	Crank	col	Comb
	710410106000	VARSHINI R	3			002	X
33	710419106009	GAYATHRI M	- Jan	Jus-	Gen	Suc	En-
34	710419106010	HARIHARAN C	Hori	Paris	Harris	Harris	Haris
35	710419106011	INDHUPRIYA S	Timber	Trata	Tretor	Inda-	Indo
36	710419106012	KAILESHWARAN S <	Time	- Zunn	17		
37	710419106013	KANNIKA C	Banke	bust	615	61	Lan
38	710419106014	KARTHIKA K	Tour H	xanflile	1000	F. Film	100
39	710419106014	pro to the total state of the to	Skar	6.00	6.10	CHE	acrit Like
39	/10419106016(F	KAVIVAS	5	5.40	3.KM	DYON	S. Kor

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F .- Taluk,



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40 710419106018 MANIKANDAN R Landa L								-
42 710419106020 MERLIN ALFINA M 43 710419106021 PAUL MOHAN M 44 710419106023 PRIYANKA P 45 710419106026 RITHICK K 51BI CHAKRAVARTHI 710419106027 B 47 710419106028 SOWBARNIKA V 48 710419106029 SOWMIYA M VASANTHA KUMAR 49 VASANTHA KUMAR	40	710419106018	MANIKANDAN R	Marfila	Marikenel	Many 1	Manify!	denipuel.
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44 710419106023 PRIYANKA P 45 710419106026 RITHICK K 46 710419106027 B 47 710419106028 SOWBARNIKA V 48 710419106029 SOWMIYA M VASANTHA KUMAR 49 VASANTHA KUMAR	42	710419106020	MERLIN ALFINA M	Marsin.	toeler	Marten	Marle-	lepla
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	49	710419106030		Jan 2	Vest	The last	Yor 2	Pay

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G 21	REGISTER	NAME OF THE	04.03.2023	
S.No	NUMBER	STUDENT	4-5,00pm	
1	710420106001	BABU A	Ration	
2	710420106002	BABY INDIRA S	Day	
3	710420106004	DHILEEPKUMAR S	Deleter Jan	
4	710420106005	DIVYAS	ivy	
5	710420106006	FIYARLIN SHARONE J	ivarling J	
6	710420106007	HARRIS RAJA X	Faris 010	
7	710420106008	JAYA SRUTHI K	Toula Softi	
8	710420106009	JEEVITHA P	Thevitte	
9	710420106010	JENIFA D	-Carifa.	
10	710420106011	JOSHUA THOMAS	Stephon .	
11	710420106012	KARTHIKA P	tactive	
12	710420106013	KAVIMANI G	TKavimani G	
13	710420106014	MALATHIM	Malstring.	
14	710420106015	METREESHIYA S	Mefexalnya	
15	710420106017	MYTHILI A	Mystori.	
16	710420106018	NATARAJAN A	Martia lin-V	
17	710420106019	NIVETHA S	Wind has	
18	710420106020	PAULABISHEK D	Dulia D.	
19	710420106021	PRADEEP S	Kader S,	
20	710420106022	PRAVEENKUMAR N	Frat 1	
21	710420106026	SANGAMITHRA T	Languar	
22	710420106028	SERMASAMY J	Speraing 2	
23	710420106030	SHYAM PRAKASH S	Keyeday)
24	710420106031	SWETHA S	Juglia.	
25	710420106032	THARANI S	Thank	
26	710420106033	VIGNESHWARAN A	19 prescando A	
27	710420106301	NAVEEN RAJ.P	Yaveen Raja	
28	710420106302	SANTHOSH.S	Sarken!	
29	710419106002	ARAVIND S E	Ivanim co	
30	710419106006	DHIVAKARAN M	Dhi rake	
31	710419106007	DINESH KUMAR S	DROPH S	
32	710419106008	GAYATHIRI VARSHINI R	halw-R	
33	710419106009	GAYATHRI M	anyathir 7	3
34	710419106010	HARIHARAN C	A TON	/
35	710419106011	INDHUPRIYA S	FELITA'IA	
36	710419106012	KAILESHWARANS	Epilema. 5	
37	710419106013	KANNIKA C	Kannilac	
38	710419106014	KARTHIKA K	Kolle.	

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	39	710419106016	KAVIYA S	Kariff
	40	710419106018	MANIKANDAN R	restolation
	41	710419106019	MATHUMITHA G	nother ?
	42	710419106020	MERLIN ALFINA M	rextury.
	43	710419106021	PAUL MOHAN M	Par l
	44	710419106023	PRIYANKA P	Sayano
	45	710419106026	RITHICK K	Rithreful-
	46	710419106027	SIBI CHAKRAVARTHI B	Sidt.
	47	710419106028	SOWBARNIKA V	South
	48	710419106029	SOWMIYA M	Dought "
	49	710419106030	VASANTHA KUMAR K	(Vals)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

MULTIPLE CHOICE QUESTION

1. In MSP430, the size of the status register is a) 2 bytes b) 1 byte c) 1 bit d) 2 bit 2. Which of the following bit/s of the status register that allows the microcontroller to operate in its low power mode?. a) CPU off b) Z c) Reserved d) N 3. What is actually done to improve the efficiency of a RISC processor? a) they have one or more registers hard wired to the commonly used values b) instructions are reduced c) they have two or more processors inbuilt connected between
d) they have many instructions that are interrelated to each other
4. To improve the efficiency of an MSP430 based microcontroller, for one register.
a) there are 4 values for four addressing modes
b) there is only one value for all addressing modes
c) there are two values for each addressing mode
d) there are 2 values for four addressing modes 5. There are number of emulated instructions found in the MSP430?
a) 4
b) 8
c) 16
d) 24
6. Pre increment addressing is available in MSP430?.
a) true
b) can't be said
c) false
d) depends on the conditions
7. Which out of the following is a correct emulated instruction?
a) ADD(.B) src,dst
b) ADDC(.B) src,dst
c) AND(.B) src,dst
d) ADC(.B) dst
8. dadd instruction can act as a) valid adder with carry Dr.M.JEYAKUMAR, M.EPh.D
a) valid adder with carry b) valid BCD addition CHRIST THE KING ENGINEERING COLLEGE,
c) all of the mentioned CHRIST THE KING ENGINEEMING CHRIST THE KING CHRIS
Chikkataminataran Tatuk.

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- d) none of the mentioned
- 9. Which of the following instructions don't affect the status bits?
- (a) bis
- (b) bic
- (c) bis & bic
- (d) bis & bic
- 10. .w form is used for operations
- (a) that uses bytes
- (b) that uses words
- (c) that uses none
- (d) that uses both

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING VAEC2307 - MSP430 STUDENTS ASSESSMENT MARKS

	REGISTER		MARKS	
S.No	NUMBER	NAME OF THE STUDENT	10	
1	710420106001	BABU A	9	
2	710420106002	BABY INDIRA S	10	
3	710420106004	DHILEEPKUMAR S	9	
4	710420106005	DIVYA S	9	
5	710420106006	FIYARLIN SHARONE J	10	
6	710420106007	HARRIS RAJA X	10	
7	710420106008	JAYA SRUTHI K	10	
8	710420106009	JEEVITHA P	9	
9	710420106010	JENIFA D	10	
10	710420106011	JOSHUA THOMAS	9	
11	710420106012	KARTHIKA P	9	
12	710420106013	KAVIMANI G	9	
13	710420106014	MALATHI M	10	
14	710420106015	METREESHIYA S	10	
15	710420106017	MYTHILI A	8	
16	710420106018	NATARAJAN A	9	
17	710420106019	NIVETHA S	10	
18	710420106020	PAULABISHEK D	10	
19	710420106021	PRADEEP S	9	
20	710420106022	PRAVEENKUMAR N	9	
21	710420106026	SANGAMITHRA T	10	
22	710420106028	SERMASAMY J	8	
23	710420106030	SHYAM PRAKASH S	9	
24	710420106031	SWETHA S	9	
25	710420106032	THARANI S	9	
26	710420106033	VIGNESHWARAN A	10	
27	710420106301	NAVEEN RAJ.P	9	
28	710420106301	SANTHOSH.S	9	
29	710419106002	ARAVIND S E	9	
30	710419106006	DHIVAKARAN M	9	
31	710419106007	DINESH KUMAR S	9	
32	710419106007	GAYATHIRI VARSHINI R	10	
33	710419106008	GAYATHRI M	10	
34	710419106010	HARIHARAN C	9	
35	710419106010	INDHUPRIYA S	10	
36	710419106011	KAILESHWARAN S	9	
37	710419106012	KANNIKA C	10	
38	710419106013	KARTHIKA K	10	
39	710419106014	KAVIYA S	10	
40	710419106018	MANIKANDAN R	9	
-41()	710419106018	MATHUMITHA G	10	

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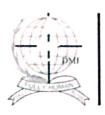
42	710419106020	MERLIN ALFINA M	9
43	710419106021	PAUL MOHAN M	9
44	710419106023	PRIYANKA P	9
45	710419106026	RITHICK K	10
46	710419106027	SIBI CHAKRAVARTHI B	9
47	710419106028	SOWBARNIKA V	10
48	710419106029	SOWMIYA M	9
49	710419106030	VASANTHA KUMAR K	9

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VAEC2307 - MSP430

	MULTIPLE CHOICE	E QUESTION	
Name Register No. Year / Sem Academic Year	: Raby. A : 710420106001 : 1111111 : 2022-23		
1. In MSP430, the size a) 2 bytes b) 1 byte c) 1 bit d) 2 bit	ze of the status register is	_·	
2. Which of the follomode?. a) CPU off b) Z c) Reserved d) N	wing bit/s of the status register tha	at allows the microcontroller	to operate in its low power
a) they have one or n b) instructions are rec c) they have two or n	lone to improve the efficiency of a nore registers hard wired to the co duced nore processors inbuilt connected structions that are interrelated to a	between	
a) there are 4 values is b) there is only one v c) there are two values	ficiency of an MSP430 based mice for four addressing modes value for all addressing modes es for each addressing mode for four addressing modes	rocontroller, for one register.	
5. There are a) 4 b) 8 c) 16 d) 24	number of emulated in	Lofan	30? AR. M.E.,Ph.D.



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6. Pre increment addressing is	available in MSP 430?.
a) true	
b) can't be said	
c) false	
d) depends on the conditions	
7. Which out of the following	is a correct emulated instruction?
a) ADD(.B) src,dst	
b) ADDC(.B) src,dst	
c) AND(.B) src,dst	
d) ADC(.B) dst	
8. dadd instruction can act as _	
a) valid adder with carry	
by valid BCD addition	
c) all of the mentioned	\sim
d) none of the mentioned	
9. Which of the following instruc	ctions don't affect the status bits?
(a) bis	
(b) bic	
(c) bis & bic	
(c) bis & bic	/
10w form is used for operation	as ,
(a) that uses bytes	2
(b) that uses words	
© that uses none	•
(d) that uses both	

10

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

STUDENT FEEDBACK FORM

Name of the value-added course: MSP430

Name of the student: P. Jeevitha

Register Number: 710420106009

Year / semester: II /VI

Batch: 2020-2024

Course Coordinator: Mas. V. Nathya Academic Year: 2022-2023

Sl.No.	Criteria	Rating				
		Excellent	Good	Average	Satisfactory	Poor
1.	Course content	V		-		
2.	Individual attention		V			
3.	Level of the course		~			
4.	Regularity and knowledge of faculty					
5.	Coverage of syllabus					
6.	Interaction		V			
7.	Outcome	V				
	Total	20	12			
	Remarks	Good				

EXCELLENT-5

GOOD-4

AVERAGE-3

SATISFACTORY-2

POOR-1

SING ENGLANDS COMPANY OF THE COMPANY

Jacobka Signature of the student

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POST EVENT REPORT

Course code	VACEC2307
Name of the course	MSP430
Duration of the course	32 Hours
Academic year	2022-2023
Period of the course	16 Days
Coordinator & Faculty handling the course	Mrs.V.Nithya Ms.L.Pavithra
Number of the students registered	49
Number of the students completed	49
% of students completed the course	100%
Details of the course	The course details the usual remediation techniques practiced worldwide and provide an understanding of the relevant theoretical concepts. The current course will enable a student to: Develop understanding of integrated approaches to remediating contaminated sites.



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DEPARTMENT OF ECE

Value Added Course on MSP430

Certificate of Completion

This is to certify that RITHICK & CTIO419106026 has successfully completed the Value Added Course on "MSP430" conducted by the Department of Electronics & Communication Engineering at Christ The King Engineering College from 13-02-2023 to 04-03-2023.

HOD / ECE

PRINCIPAL

DEMLJEYAKU

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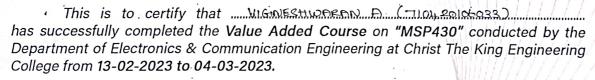
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Value Added Course on M5P430

Certificate of Completion



Vne HOD / ECE



Dr.M.JEYAKI

m Village, iayam Taluk,

Combatore - 641 104.